



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

In re Application of:
HAYES ET AL.

Serial No.: **10/604,107**

Filed: **JUNE 26, 2003**

Title: **METHOD AND APPARATUS FOR
PERFORMING INPUT/OUTPUT FLOOR
PLANNING ON AN INTEGRATED CIRCUIT
DESIGN**

§ Attorney Docket No.: **BUR920020055US1**

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Examiner: **UNKNOWN**

Group Art Unit: **2836**

LETTER TO THE OFFICIAL DRAFTSMAN

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith are FIVE (5) sheets of formal drawings for the above-identified application.

CERTIFICATE OF MAILING	
37 CFR 1.8(a)	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.	
Date <u>11/4/03</u>	Signature of <u>Catherine D. Nelson</u> Signature of Catherine D. Nelson

Respectfully submitted,

Antony P. Ng
Registration No. 43,427
BRACEWELL & PATTERSON, LLP
P.O. Box 969
Austin, Texas 78767
(512) 472-7800
ATTORNEY FOR APPLICANTS